

JIC714 U.S. PTO  
6/617450

		Subclass
	Class	

**ISSUE CLASSIFICATION**

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PATENT NUMBER

**U.S. UTILITY Patent Application**

② <i>JB</i>	O.I.P.E. <i>AA</i>	PATENT DATE <i>CS 2</i>
SCANNED	Q.A.	

APPLICATION NO. 09/617450	CONT/PRIOR F	CLASS 375	SUBCLASS 239	ART UNIT 2634	EXAMINER TSC
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ANTS Frank Sattler  
Walter Klumb

Method for modulating a basic clock signal for digital circuits and clock modulator for implementing the method

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

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